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ABSTRACT OF THE DISCLOSURE

In a solid-state image-sensing device, first a switch Swa is turned on to sample and hold an image signal in a capacitor Ca, and then the switch Swa is turned off. Next, a switch SWb is turned on to sample and hold a noise signal in a capacitor Cb, and then the switch SWb is turned off. Next, switches SW1a and SW2a are turned on simultaneously so that the image signal in the capacitor Ca is fed through a buffer 6 to a capacitor Cc, and then the switches SW1a and SW2a are turned off. Then, a switch SW3 is turned on to reset the input side of the buffer 6. Next, switches SW1b and SW2b are turned on simultaneously so that the noise signal in the capacitor Cb is fed through the buffer to a capacitor Cd, and then the switches SW1b and SW2b are turned off. Then, the switch SW3 is turned on to reset the input side of the buffer 6 again.